

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT			ATTY DOCKET NO. 944-003.108	SERIAL NO. 09/965,657		
			APPLICANT: Jari Melava			
			FILING DATE: September 26, 2001	ART UNIT: 2001 2637		
UNITED STATES PATENT DOCUMENTS						
EXAM. INITIAL	DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>eh</i>	5,189,685	02/23/1993	Jaffard et al.			
<i>eh</i>	5,970,110	10/19/1999	Li			
<i>eh</i>	6,157,694	12/05/2000	Larsson			
<i>eh</i>	6,281,759	08/28/2001	Coffey			
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FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
<i>eh</i>	WO 01/45263 A1	06/21/2001	International			No
OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)						
<i>eh</i>	CRANINCKX J ET AL: "A 1.75-GHZ/3-V DUAL-MODULUS DIVIDE-BY-128/129 PRESCALER IN 0.7-MUM CMOS", IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE INC. NEW YORK, US, VOL. 31, NO. 7, JULY 1, 1996, PAGES 890-897, XP000632373					
Examiner (To be assigned) <i>Edo Chang</i>			Date: 4/5/05			